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APPLICATION NO. FILING DATE		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/823,529	10/823,529 04/13/2004		Bo Jin	CD03021	4879		
	7590	03/22/2006		EXAM	EXAMINER		
WALKER &	& SAK(O, LLP	LAM, D	LAM, DAVID			
Suite 235 300 South Fi	rst Street	i.	ART UNIT	PAPER NUMBER			
San Jose, CA 95113				2827			
	•				DATE MAILED: 03/22/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

					8				
		Application No.		Applicant(s)					
		10/823,529		JIN ET AL.					
	Office Action Summary	Examiner		Art Unit					
		David Lam		2827					
Period fo	The MAILING DATE of this communication or Reply	appears on the cove	r sheet with the c	orrespondence ad	Idress				
THE I - Exter after - If the - If NO - Failu	ORTENED STATUTORY PERIOD FOR REMAILING DATE OF THIS COMMUNICATIOnsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a period for reply is specified above, the maximum statutory per te to reply within the set or extended period for reply will, by stated the period by the Office later than three months after the material part of the provided patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, howeverther the statutory minimal will apply and will expire atute, cause the application to	ever, may a reply be tin nimum of thirty (30) day SIX (6) MONTHS from o become ABANDONE	nely filed s will be considered timel the mailing date of this c D (35 U.S.C. § 133).	y. ommunication.				
Status									
1) 🛛	Responsive to communication(s) filed on 1/	10/06 .							
,—	This action is FINAL . 2b) ☐ This action is non-final.								
3)⊠	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.								
Dispositi	on of Claims								
5)⊠ 6)□ 7)□	 ✓ Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) 8-20 is/are withdrawn from consideration. ✓ Claim(s) 1-7 is/are allowed. ☐ Claim(s) is/are rejected. ☐ Claim(s) is/are objected to. ☐ Claim(s) are subject to restriction and/or election requirement. 								
Applicati	on Papers								
10)	The specification is objected to by the Examement The drawing(s) filed on is/are: a) and a specificant may not request that any objection to the Replacement drawing sheet(s) including the control of the oath or declaration is objected to by the	accepted or b) obj the drawing(s) be held rection is required if th	in abeyance. See e drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 C					
Priority ι	ınder 35 U.S.C. § 119								
12) <u> </u>	Acknowledgment is made of a claim for fore All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bur See the attached detailed Office action for a	ents have been rece ents have been rece priority documents ha reau (PCT Rule 17.2	eived. eived in Applicati ave been receive (a)).	on No ed in this National	Stage				
Attachmen	t(s)								
2) Notice 3) Information	te of References Cited (PTO-892) te of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB. or No(s)/Mail Date	/08) 5)	Interview Summary Paper No(s)/Mail D Notice of Informal F Other:		O-152)				

This application is in condition for allowance except for the following formal matters:

IN THE CLAIM:

- Claims 8-20 should be cancel due the final election/restriction set for in the last office

action.

Allowable Subject Matter

Claims 1-7 are allowable over the prior art of record because none of the prior art

whether taken singularly or in combination, especially when these limitations are considered

within the specific combination claimed, to teach: a memory cell comprising: a capacitor,

among others claimed in independent claim 1, having plates coupled between a first node and a

second node, a portion of at least one plate of the capacitor comprising a first interconnect wiring

formed over a gate layer and includes a plurality of conductive layers that interconnects circuit

components of the memory cell.

Conclusion

Prosecution on the merits is closed in accordance with the practice under Ex parte

Quayle, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS**

from the mailing date of this letter.

Art Unit: 2827

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Lam whose telephone number is 571-272-1782. The examiner can normally be reached on 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zarabian Amir can be reached on 571-272-1852852. The fax phone numbers for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Lam

March 19, 2006

DAVID LAM